

ABSTRACT OF THE DISCLOSURE

A flat panel display capable of preventing inline short between adjacent wirings and voltage drop through power supply line by using pixel electrode layer as a power supply layer, and a fabrication method thereof. A flat panel display of the present invention is made up of a thin film transistor including source/drain electrodes, formed on an insulation substrate, an insulation film formed on the insulation substrate including the thin film transistor and including first and second contact holes for exposing the source/drain electrodes respectively, a pixel electrode formed on the insulation film and connected to one of the source/drain electrodes through one of the first and second contact holes, and a power supply layer formed on the insulation film and connected to the other one of the source/drain electrodes through the other one of the first and second contact holes. Losses in the power supply line is reduced by forming the power supply line of a low resistivity material and by providing the power supply line in a grid structure.